

THE UNITED STATES PATENT AND TRADEMARK OFFICE (Case No. RA043D2DC)

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In	the	Appl	ica	tion	of:

FARMWALD ET AL.

Technology Center 2100

Serial No: 09/545,648

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Group

Before

Filed: APRIL 10, 2000

Examiner: G. Auve'

Art Unit: 2181

Title: SYSTEM HAVING DOUBLE DATA

TRANSFER RATE AND INTEGRATED

CIRCUIT THEREFOR

Assistant Commissioner for Patents Washington, DC 20231

AMENDMENT

Dear Sir:

Kindly amend the application as follows:

IN THE CLAIMS:

Kindly Amend the following claims:

In claim 170, line 1, after circuit inset --device--.

In claim 171, line 1, delete "system" and substitute --integrated

circuit device--.

In claim 179, line 1, delete "system" and substitute --integrated circuit device--.

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